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Load symmetrising e.g. for several power supply modules including rectifier modules - involves checking each module to assess whether bus functions in recessive or dominant condition

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Number of Countries: 021 Number of Patents: 007

Patent Family:

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Patent No	Kind	Date	Ap	plicat No	Kind	Date	Week	
DE 19704662	A1	19980813	DE	1004662	Α	19970207	199838	В
WO 9835420	A1	19980813	WO	98DE240	Α	19980127	199838	
AU 9863906	A	19980826	AU	9863906	Α	19980127	199902	
EP 958645	Al	19991124	ΕP	98909289	Α	19980127	199954	
			WO	98DE240	Α	19980127		
CN 1243611	Α	20000202	CN	98801812	Α	19980127	200025	
EP 958645	B1	20011017	EP	98909289	Α	19980127	200169	
			WO	98DE240	Α	19980127		
DE 59801760	G	20011122	DE	501760	Α	19980127	200201	
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Abstract (Basic): DE 19704662 A

A method of load symmetrisation of several autarkic and independently operating power supply modules (4,6,8) of a modular power supply plant (2) and networked by nodal point bus topology, involves initially checking a bus (10) of each module, with an arbitration process initiated when the bus can be accessed for a given period of time (Tm). Each module (4,6,8) is checked to determine whether the bus (10) resides in a recessive or dominant condition at the commencement of arbitration, to decide on slave or master status for the module, respectively.

The master module (4,6,8) then generates a load information signal with each slave module (4,6,8) evaluating the received signal such that a desired value of its output voltage is changed so that a detached load asymmetry is adjusted.

USE - Modular design of current (power) supply equipment. ADVANTAGE - Simplified method for load symmetrisation, using digital transmission of load symmetrisation, using digital transmission of load balancing information.

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